IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Tremblay, Marc; Joy, William

Assignee:

Sun Microsystems, Inc.

Title:

Local And Global Register Partitioning In A VLIW Processor

Application No.:

09/204,585

Filing Date:

December 3, 1998

Examiner:

David Y. Eng

Group Art Unit:

2155

Docket No.:

SP-3288 US

Newport Beach, California March 7, 2002

COMMISSIONER FOR PATENTS

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(c) WITH FEE

MAR 2 0 2002

Dear Sir:

Technology Center 2 100

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) wish to call the following documents (copies enclosed) to the attention of the Examiner for the aboveidentified patent application.

A form PTO-1449 listing these documents is enclosed.

The relevance of any non-English language documents cited herein is explained in the accompanying copy of the abstract.

Citation of the above documents shall not be construed as:

- an admission that the documents are necessarily prior art with respect to the 1. instant invention;
  - 2. a representation that a search has been made; or
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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Application No. 09/204,585

03/14/2002 JBALINAN 00000118 192386

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Respectfully submitted,

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